Senior Software Engineer for Analog/Mixed-Signal Verification Environments (f/m/div)*

Job description

Infineon’s engineers develop state-of-the-art semiconductor solutions. This is only possible with a cutting-edge design flow as a basis. Do you want to lay the groundwork for how Infineon’s products of the future are optimized for robustness, power efficiency and highest performance? With this position you can influence pre-silicon verification methodology used by Infineon’s designers worldwide by bringing up new innovative software solutions.

In your new role you will:

- Develop our software tools for automated simulation-based verification of analog/mixed signal circuits
- Improve existing software modules including refactoring and maintenance of existing code
- Train and support our worldwide user community
- Write technical documentation and strive for quality by complying to all relevant QA processes
- Challenge our existing verification processes and devise better and more efficient ways of IC product development and verification
- Develop and integrate new or adapted verification methods and software tooling into our established design flow

Profile

An Infineon user is your first priority – we want to develop functionalities that help in the daily chip development process. Therefore, it is important to ‘translate’ user requirements into specifications – while having an eye for the details. As we are working in a team, but also in standalone projects, you should bring flexibility in your working style as well as good self-management.

You are best equipped for this task if you have:

- A degree in Computer Science, Electrical Engineering or related areas
- At least 1-3 years of professional work experience in software design and implementation or electronic design automation (EDA) or a related technical area
- Good programming skills with focus on object-oriented software development (e.g. Java, C++) in Unix environments and familiarity with working and scripting on Linux; Perl skills are an advantage

At a glance

Location: Munich (Germany)
Job ID: 334266
Start date: as soon as possible
Entry level: 1-3 years
Type: Full time
Contract: Permanent

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Contact

Alexander Greie
Team Lead Talent Attraction
• **Familiarity with agile software development techniques**
• **Some hands-on experience with EDA environments** (e.g. Cadence Virtuoso/ADE) and simulation of **analog/mixed-signal circuits** (e.g. Spectre, HSPICE) is a benefit
• **Fluent English skills with German** seen as a plus

**Benefits**

• **Munich:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International Assignments; Different Career Paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home Office Options; Part-time work possible; Sabbatical; Creche and kindergarten with 180 spots and opening times until 6pm; Holiday Child Care; On-site social counselling and works doctor; Health promotion programs; On-site gym, jogging paths, beachvolleyball, tennis and soccer court; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement; Performance bonus; Cheaper ticket for public transport and very own S-Bahn station; Accessibility access for wheelchairs

**Why Us**

**Part of your life. Part of tomorrow.**

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

The central R&D organization „Design Enabling and Services“ (DES) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon’s advanced IC development from early high level system models to verified products ready for manufacturing.

* The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.