

JOB DESCRIPTION JUNIOR LOGIC DESIGN ENGINEER

OVERVIEW

Are you looking for a great opportunity to start your career after completing your university studies?

Rambus, a premier chip and silicon IP provider, is seeking to hire a Junior Logic Design Engineer to join our team in Vught as part of the Silicon IP Business Unit. Candidates will be joining some of the brightest inventors and engineers in the world to develop products that make data faster and safer.

The Junior Logic Design Engineer position is a Full-Time position, and you will be reporting to the local R&D Team Lead.

Your responsibilities:

- Design and Implementation of secure silicon IPs
- Implementation: RTL design, lint, synthesis, IP release
- Verification: execute basic verification and assist verification team to complete the IP verification
- Participate in technical reviews
- Preparation of product documentation
- Interact with fellow engineers to help ensure successful development of high value products

What's in it for you:

- A great opportunity to develop your skills (technical, soft skills, communication, etc.)
- Gaining experience in a multinational and diverse environment

Requirements/Qualifications:

- Bachelor or Master degree in Hardware Engineering, Computing Science or comparable
- Good level of English is required (team interaction, meeting attendance, reading and writing of documentation)
- Previous internship experience in the IP or chip industry is a plus
- Experience with FPGA is a plus
- Affinity with Security/Cryptography is a plus

For more information about Rambus, visit rambus.com. For additional information on life at Rambus and our current openings, check out rambus.com/careers/.